

2189 \$
Patent

Docket No.: TRAN-P034

Information Disclosure Statement Transmittal

I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner of Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the below date of deposit.

Date of Deposit:	08/20/03	Name of Person Making the Deposit:	KATHERINE RINALDI	Signature of the Person Making the Deposit:	<i>Katherine Rinaldi</i>
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Sameer Halepete, H. Peter Anvin, Zong Chen, Godfrey P. D'Souza, Marc Fleishmann, Keith Klayman, Thomas Lawrence and Andrew Read

Serial No.: 09/484,516

Examiner: Myers, Paul R.

RECEIVED

Filed: 01/18/00

Art Unit: 2189

AUG 29 2003

For: ADAPTIVE POWER CONTROL

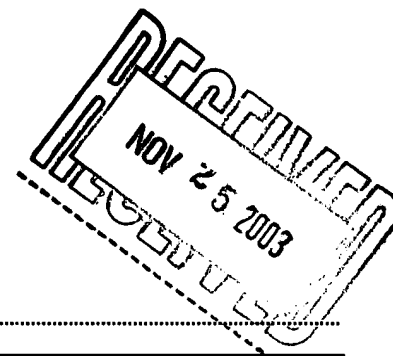
Technology Center 2100

Commissioner of Patents
P. O. Box 1450
Alexandria, VA 22313-1450
Sir:

Information Disclosure Statement Transmittal

Transmitted herewith is the following:

- Formal drawings, totaling sheets.
- Informal drawings, totaling sheets.
- Certification for PTO Consideration
- Information Disclosure statement (sheets)
- ☒ Information Disclosure statement and late filing fee
- ☒ Form 1449
- Petition for Extension of Time
- ☒ Other: REFERENCES



Fee Calculation (for other than a small entity)

Fee Items	Fee Rate	Total
Petition for Extension of Time (fee calculated elsewhere)	\$.00	\$0.00
Information Disclosure Statement, late filing	\$180.00	\$180.00
Other:		\$0.00
Total Fees		\$180.00

PAYMENT OF FEES

1. The full fee due in connection with this communication is provided as follows:
 - ☒ The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085.
A duplicate copy of this authorization is enclosed.
 - ☒ A check in the amount of \$180.00
 - ☐ Charge any fees required or credit any overpayments associated with this filing to Deposit Account No.: 23-0085.

Please direct all correspondence concerning the above-identified application to the following address:


WAGNER, MURABITO & HAO LLP
Two North Market Street, Third Floor
San Jose, California 95113
(408) 938-9060

Respectfully submitted,

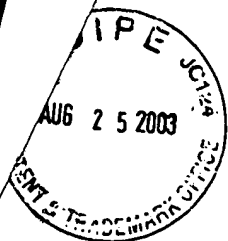
Date: _____

8/20/03

By: _____



Ronald M. Pomeranke
Reg. No. 43,009



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: TRAN-P034

Inventor(s): Halepete, et al.

Group Art Unit:

Filed: 01/18/00

Examiner:

Serial No.: 09/484,516

Title: ADAPTIVE POWER CONTROL

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AUG 29 2003

Technology Center 2100

Commissioner of Patents
P. O. Box 1450
Alexandria, VA 22313-1450
Sir:

Information Disclosure Statement Submitted Pursuant to 37 C.F.R. 1.97(b)

The citations referenced herein, copies attached, may be material to the examination of the above-identified application and are, therefore, submitted in compliance with the duty of disclosure as defined in 37 C.F.R. 1.56. The Examiner is requested to make these citations of official record in the application.

This Information Disclosure Statement submitted in accordance with 37 C.F.R. 1.97(b) is not to be construed as a representation that a search has been made, that additional items material to the examination of this application do not exist, or that any one or more of these citations constitute prior art under 35 U.S.C. 102.

The Examiner's attention is respectfully directed to the following U.S. Patents:

<u>Pat. No.</u>	<u>Pat. Title</u>	<u>Grant Date</u>
5,560,020	POWER SAVING PROCESSING SYSTEM	09/24/96
5,825,674	POWER CONTROL FOR MOBILE ELECTRONICS USING NO- OPERATION INSTRUCTIONS	10/20/98
5,884,049	INCREASED PROCESSOR PERFORMANCE COMPARABLE TO A DESKTOP COMPUTER FROM A DOCKED PORTABLE COMPUTER	03/16/99
5,894,577	INTERRUPT CONTROLLER WITH EXTERNAL IN-SERVICE INDICATION FOR POWER MANAGEMENT WITHIN A COMPUTER SYSTEM	04/13/99
5,933,649	METHOD AND DEVICE FOR CONTROLLING A CPU STOP CLOCK INTERRUPT	08/03/99
6,202,104	PROCESSOR HAVING A CLOCK DRIVEN CPU WITH STATIC DESIGN	03/13/01
6,272,642	MANAGING A SYSTEM'S PERFORMANCE STATE	08/07/01
6,279,048	SYSTEM WAKE-UP BASED ON JOYSTICK MOVEMENT	08/21/01
6,304,824	VOLTAGE CONTROL OF INTEGRATED CIRCUITS	10/16/01
6,314,522	MULTI-VOLTAGE LEVEL CPU MODULE	11/06/01
6,442,746	PREVENTING DAMAGING OF LOW VOLTAGE PROCESSOR IN HIGH VOLTAGE SYSTEM	08/27/02

The Examiner's attention is respectfully directed to the following Published Foreign Patent Applications:

Foreign Patent or Published Foreign Patent Application

Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
					Yes	No
JP409185589A	07/15/97	JAPAN	G06F	15/16		X
EP0501655A2	02.09.92	EPO	G06F	1/32	X	

08/27/2003 ZJUHA1 00000016 230085 09484516

01 FC:1806 180.00 DA

Void date: 08/27/2003 ZJUHA1
08/27/2003 ZJUHA1 00000016 230085 09484516
01 FC:1806 180.00 CR

rev. 6/97 jpw

08/27/2003 ZJUHA1 00000017 09484516 180.00 OP
01 FC:1806

The Examiner's attention is respectfully directed to the following related documents:

Desai et al.; " SIZING OF CLOCK DISTRIBUTION NETWORKS FOR HIGH PERFORMANCE CPU CHIPS";
Digital Equipment Corp., Hudson, MA; pgs 389-394; 1996

Please direct all correspondence concerning the above-identified application to the following address:


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(408) 938-9060

Respectfully submitted,

Date: _____

8/20/03

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